

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: June LEE

Serial No.: 10/630,434 Examiner: Le, Thong Quoc

Filed: July 29, 2003 Group Art Unit: 2827

Confirmation No.: 7848

For: SEMICONDUCTOR MEMORY DEVICE INFORMING INTERNAL  
VOLTAGE LEVEL USING READY/BUSY PIN

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

Responsive to the Office Action, Paper No. 6, dated June 9, 2006, please amend the application as follows.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 8 of this paper.